IN THE SPECIFICATION

Please amend the specification as follows:

Please amend the first paragraph on page 1 after the title (previously inserted by preliminary amendment filed on February 25. 2004):

CROSS REFERENCE TO RELATED APPLICATIONS

This patent application is related to U.S. Patent Application Serial Number ————— 10/789,895 (Attorney Docket No. 303.884US1), entitled ~~METHOD AND SYSTEM FOR CORRELATING AND COMBINING PRODUCTION AND NON-PRODUCTION DATA FOR ANALYSIS~~ METHOD AND SYSTEM FOR AGGREGATING AND COMBINING MANUFACTURING DATA FOR ANALYSIS, to Naoki Toyoshima, ~~Shinichi Murakami~~ and Yuko Maeda, assigned to Micron Technology, Inc., and incorporated herein by reference.

Please amend the paragraph beginning at page 12, line 3 as follows:

In table A, ~~tS$_{i,j,k,...}$~~ tS$_{1,2,3,...}$ is defined as the time when a data sample is taken. In table A, tL$_{i,j,k,...}$ is defined as the time when a production lot process is begun. In table A, ~~S$_{i,j,k,...}$~~ S$_{1,2,3,...}$ is defined as the data sampled. In table A, L$_{i,j,k,...}$ is defined as the lot data calculated. In the present invention the calculation performed to arrive at L$_{i,j,k,...}$ is a weighted mean calculation.

Please amend the paragraph beginning at page 15, line 24 as follows:

The computer **702** may include a processor **730**, a storage device **740**, a communications interface device **711**, an ~~input~~ output device **750**, and an ~~output~~ input device **760**, all connected via a bus **770**.

Please amend the paragraph beginning at page 15, line 27 as follows:

The processor **730** may represent a central processing unit of any type of architecture, such as a CISC (Complex Instruction Set Computing), RISC (Reduced Instruction Set Computing), VLIW (Very Long Instruction Word), or a hybrid architecture, although any appropriate processor may be used. The processor **730** may execute instructions and may

include that portion of the computer **702** that controls the operation of the entire computer. Although not depicted in FIG. **7**, the processor **730** typically includes a control unit that organizes data and program storage in memory and transfers data and other information between the various parts of the computer **702**. The processor **730** may receive data from the input device ~~750~~ **760**, may read and store code and data in the storage device **740**, may send data to the output device ~~760~~ **750**, and may send and receive code and/or data to/from the network **710**.

Please amend the paragraph beginning at page 17, line 1 as follows:

The input device ~~750~~ **760** may be a keyboard, pointing device, mouse, trackball, touchpad, touchscreen, keypad, microphone, voice recognition device, or any other appropriate mechanism for the user to input data to the computer **702**. Although only one input device ~~750~~ **760** is shown, in another embodiment any number and type of input devices may be present.

Please amend the paragraph beginning at page 17, line 6 as follows:

The output device ~~760~~ **750** is that part of the computer **702** that communicates output to the user. The output device ~~760~~ **750** may be a cathode-ray tube (CRT) based video display well known in the art of computer hardware. But, in other embodiments the output device ~~760~~ **750** may be replaced with a liquid crystal display (LCD) based or gas, plasma-based, flat-panel display. In another embodiment, the output device ~~760~~ **750** may be a speaker. In still other embodiments, any appropriate output device suitable for presenting data may be used. Although only one output device ~~760~~ **750** is shown, in other embodiments, any number of output devices of different types or of the same type may be present.